



EV633261977

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/757,253
Confirmation No. 4387
Filing Date January 13, 2004
Inventor..... Denise M. Eppich et al.
Assignee..... Micron Technology, Inc.
Group Art Unit..... 2823
Examiner H. Lee
Attorney's Docket No. MI22-2467
Customer No. 021567
Title: Methods of Forming Transistor Devices and Capacitor Constructions

INFORMATION DISCLOSURE STATEMENT to accompany RCE

References -- See Attached Form PTO-1449

The attached Form PTO-1449 is submitted in compliance with 37 CFR §1.56. Pursuant to FEDERAL REGISTER, Vol. 69, No. 182, pg. 56542 (September 21, 2004), no copies of any cited U.S. patents or U.S. published applications are included herewith. No admission is made regarding whether the listed references are prior art.

Citation of these references is respectfully requested.

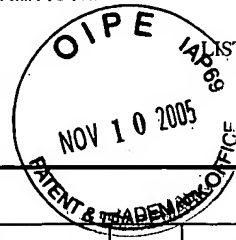
Dated: 11/10/05

Respectfully submitted,

By: [Signature]
David G. Latwesen, Ph.D.
Wells St. John P.S.
Reg. No. 38,533

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Form PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
MI22-2467SERIAL NO.
10/757,253LIST OF ART CITED BY APPLICANT
(Use several sheets if necessary)APPLICANT
Denise M. Eppich et al.FILING DATE
Jan. 13, 2004 [RCE filed herewith]GROUP
2823

U.S. PATENT DOCUMENTS

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
AA	6,214,724 B1	04/01	Nakajima			
AB	6,297,539 B1	10/01	Ma et al.			
AC						
AD						
AE						
AF						
AG						
AH						
AI						
AJ						
AK						
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FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
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AN							
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

AP		
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EXAMINER

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